

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Seiichiro KANNO *et al.*
Serial No. : Unassigned (§53(b) Cont. of 09/799,527 filed 3/7/2001)
Filed : 07 October 2003
For : SEMICONDUCTOR MANUFACTURING APPARATUS
AND METHOD OF PROCESSING SEMICONDUCTOR
WAFER USING PLASMA AND WAFER VOLTAGE PROBE
Art Unit : Unassigned (Parent - 1763)
Examiner : Unassigned (Parent - Ram N. Kackar)
Conf. No. : Unassigned (Parent - 4591)

PRELIMINARY AMENDMENT

Mail Stop Patent Application

Commissioner for Patents
POB 1450
Alexandria, Virginia 22313-1450

7 October 2003

Sir:

Prior to calculating the filing fee for the above-identified continuation, the following amendments and remarks are respectfully submitted.

In accordance with the revised format of the manner of making amendments under 37 CFR §1.121 as set forth in the Final Rule effective 30 July 2003, each section of amendment herein begins on a new page, and changes are shown by strike-through (or double brackets where appropriate) and underlining to indicate deletions and additions, respectively. A complete listing of all claims ever presented in the application is given with the current status of each claim, and only the text of all pending and withdrawn claims is presented in full, with those not being amended herein being presented in clean version.